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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

Complete if Known	
Application Number	
Filing Date	07 October 2003 (herewith)
First Named Inventor	Christophe Pierrat
Group Art Unit	
Examiner Name	
Attorney Docket Number	FTIS 1003-1

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	C12	FRERICHS, M.R., "Precise Extraction of Ultra Deep Submicron Interconnect Parasitics with Parameterized 3D-Modeling," Proceedings of the 2001 conference on Asia South Pacific design automation, Yokohama, Japan, 50 - 56.	
	C13	SCHELLENBERG, F.M., et al., "Adoption of OPC and the Impact on Design and Layout," DAC 2001, June 18-22, 2001, Las Vegas, NV, 4 pages	
	C14		

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Sheet 1 of 2

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	C1	SMITH, TABER H., et al., "A CMP Model Combining Density and Time Dependencies," Proc. CMP-MIC, Santa Clara, CA Feb. 1999 8 pages	
	C2	STROLENBERG, CHRIS W., et al., "Stay Away from Minimum Design-Rule Values," ACM SIGDA CD-ROM Project, March 27-30, 2000, 2 pages	
	C3	GROBMAN, W., et al., "Reticle Enhancement Technology: Implications and Challenges for Physical Design," DAC 1001, June 18-22, 2001, Las Vegas, NV, 6 pages.	
	C4	GOLD, SPENCER M., et al., "A Quantitative Approach to Nonlinear Process Design Rule Scaling," Proceedings of Advanced Research in VLSI, March 21-24, 1999, pp. 99-112.	
	C5	DOONG, KELVIN Y., et al., "Scaling Variance, Invariance and Prediction of Design Rule: From 0.25-um to 0.10-um Nodes inthe Era of Foundry Manufacturing," IEEE, 2001, 38-42.	
	C6	CREPEAU, J., et al., "Some Results on Yield and Local Design Rule Relaxation," IEEE 1993, 144-151.	
	C7	TIAN, RUIQI, et al., "Model-Based Dummy Feature Placement for Oxide Chemical-Mechanical Polishing Manufacturability," DAC 2000, Los Angeles, CA 4 pages.	
	C8	RIEGER, MICHAEL L., et al., "Layout Design Methodologies for Sub-Wavelength Manufacturing," DAC 2001, June 18-22, 2001, Las Vegas, NV 4 pages.	
	C9	JEPPSON, KJELL O., et al., "Formal Definitions of Edge-Based Geometric Design Rules," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 120(1) Jan. 1993, 59-69.	
	C10	Tu Wien, "An Introduction to the ASIC Technology and Design Flow," ICT Institute of Computer TEchnology, Feb 26, 2003, 29 pages.	
	C11	"MOSIS Scalable CMOS (SCMOS) Design Rules (Revision 8.0), MOSIS, Marina Del Rey, CA April 25, 2003, 51 pages.	

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EXPRESS MAIL LABEL NO. EV271023813US
DATED: 07 OCTOBER 2003 **ATTORNEY DOCKET: FTIS 1003-1**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Inventors: **Christophe Pierrat et al.** Application No. _____ Filed: **07 October 2003 (herewith)** Title: **Design-Manufacturing Interface Via a Unified Model**	Group Art Unit: unknown Examiner: unknown **CUSTOMER NO. 22470**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56.

Enclosed with this statement is a Form PTO-SB/08. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. 609.

Also enclosed with this statement is a copy of each cited document as required by 37 C.F.R. 1.98.

This statement should be considered under 37 C.F.R. 1.97(**b**) because it is being filed within three months of the filing date of an application other than a continued prosecution application under 37 C.F.R. 1.53(d).

Fee Authorization. The Commissioner is hereby authorized to charge underpayment of any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (FTIS 1003-1). A copy of this authorization is enclosed.

Respectfully submitted,

HAYNES BEFFEL & WOLFELD LLP

Date: 7 Oct 2003 By: _____
Mark A. Haynes, Reg. No. 30,846

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EXPRESS MAIL LABEL NO. EV271023813US
DATED: 07 OCTOBER 2003 **ATTORNEY DOCKET: FTIS 1003-1**

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